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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application Number	09/854,905
		Filing Date	May 15, 2001
		First Named Inventor	MOORE, Brian
		Group Art Unit	2829
		Examiner Name	
Total Number of Pages in This Submission		Attorney Docket Number	11157-23

ENCLOSURES (check all that apply)			
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group	
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences	
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	BERESKIN & PARR	
Signature		
Date	May 9, 2002	

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date:

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May 9, 2002

Stephen M. Beney B.Sc. (Physics)
416 957 1697 sbeney@bereskinparr.com

Your Reference: 09/854,905
Our Reference: 11157-23

**SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT**

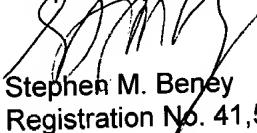
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Dear Sir:

Re: Patent Application No. 09/854,905
For: WIRELESS RADIO FREQUENCY TECHNIQUE DESIGN AND METHOD
FOR TESTING OF INTEGRATED CIRCUITS AND WAFERS
Applicant: MOORE, Brian
Filed: May 15, 2001
Group: 2829

In accordance with 37 CFR 1.56 and 1.97, applicant hereby submits a Supplemental Information Disclosure Statement and provides an English translation of a foreign document (WO 99/17353) that was provided in an earlier Information Disclosure Statement. Form PTO-1449 is also enclosed. Please have these documents recorded against the above-mentioned application.

Respectfully submitted,


Stephen M. Beney
Registration No. 41,563

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